N-Channel Super Trench Power MOSFET

Description

The HMS65N15KA uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

● VDS =150V,ID =65A

 $R_{DS(ON)} = 15.5 m\Omega \text{ (Typ)} \text{ @ Vgs} = 10V$ $R_{DS(ON)} = 17.5 m\Omega \text{ (Typ)} \text{ @ Vgs} = 4.5V$

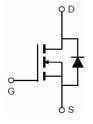
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

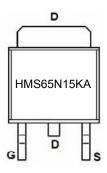
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED!

100% ΔVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

 		<u> </u>				
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity	
HMS65N15KA	HMS65N15KA	TO-252-2L	-	-	=	

Absolute Maximum Ratings (T_c=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	65	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	45.5	Α
Pulsed Drain Current	I _{DM}	195	Α
Maximum Power Dissipation	P _D	300	W
Derating factor		2	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	1296	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$



Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	DJC 0.5	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

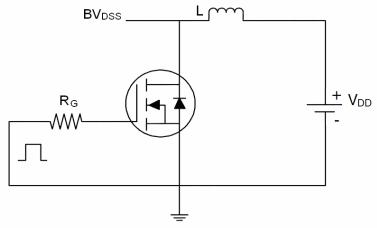
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =150V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)			•			•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.5	1.9	2.5	V
Danie Course On Otata Danietana		V _{GS} =10V, I _D =55A	-	15.5	19	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =55A	-	17.5	23	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =55A	70	-	-	S
Dynamic Characteristics (Note4)	<u> </u>		.			
Input Capacitance	C _{lss}	75/// 0//	-	10000	-	PF
Output Capacitance	C _{oss}	V_{DS} =75V, V_{GS} =0V, F=1.0MHz	-	2046	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	55	-	PF
Switching Characteristics (Note 4)			•	•		•
Turn-on Delay Time	t _{d(on)}		-	30	-	nS
Turn-on Rise Time	t _r	V_{DD} =75 V , I_D =55 A	-	52	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =4.7 Ω	-	69	-	nS
Turn-Off Fall Time	t _f		-	21	-	nS
Total Gate Charge	Qg	\/ 75\/ 55A	-	150		nC
Gate-Source Charge	Q_{gs}	$V_{DS}=75V,I_{D}=55A,$	-	50		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	26		nC
Drain-Source Diode Characteristics			•			•
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _F = I _S	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	65	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C$, $I_F = I_S$	-	140		nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	498		nC

Notes:

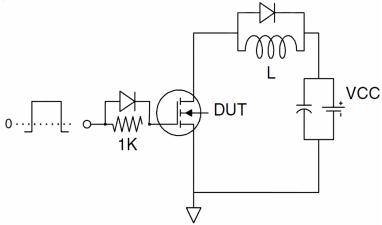
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V_DD=50V,V_G=10V,L=0.5mH,Rg=25 Ω

Test Circuit

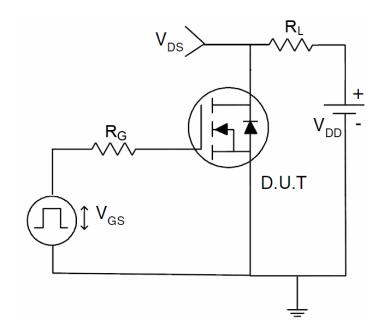
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





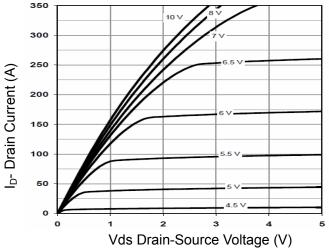


Figure 1 Output Characteristics

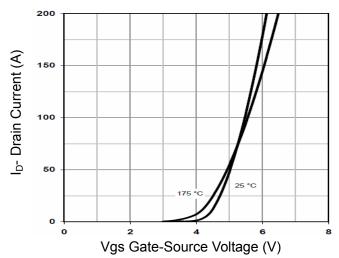


Figure 2 Transfer Characteristics

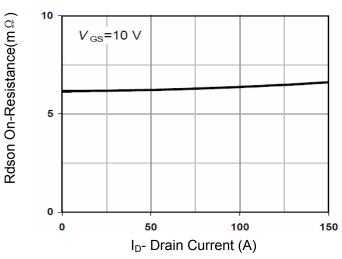


Figure 3 Rdson- Drain Current

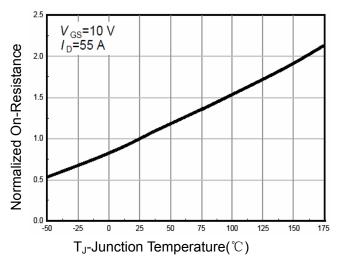


Figure 4 Rdson-JunctionTemperature

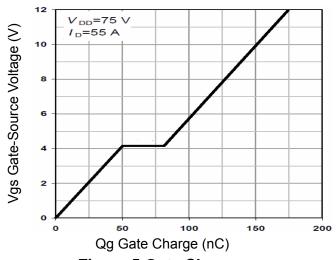


Figure 5 Gate Charge

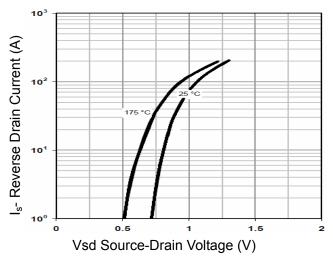


Figure 6 Source- Drain Diode Forward

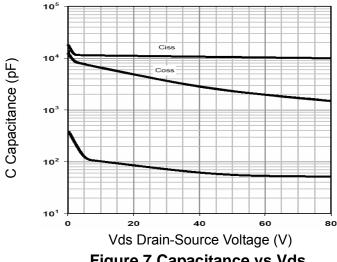


Figure 7 Capacitance vs Vds

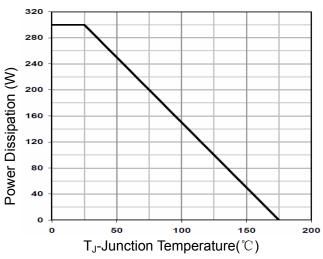


Figure 9 Power De-rating

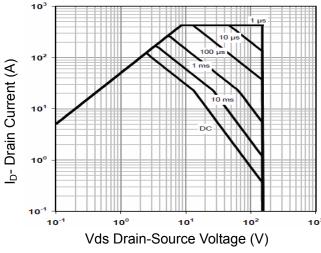


Figure 8 Safe Operation Area

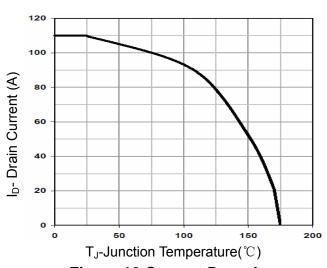


Figure 10 Current De-rating

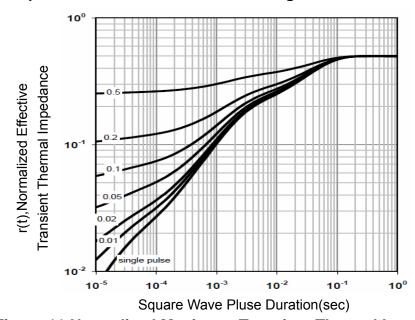
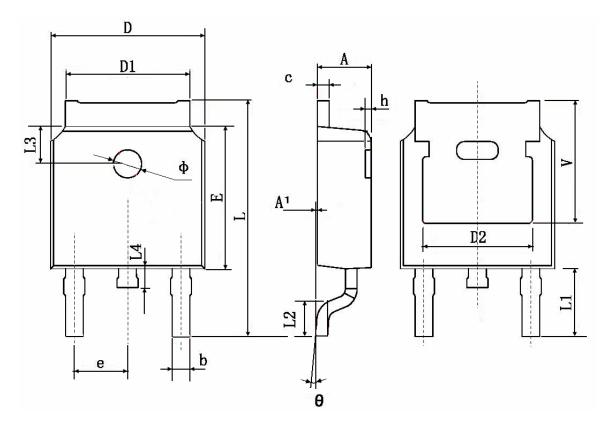


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252 Package Information



Course had	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 TYP.		0.190 TYP.		
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		

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